



PATENT APPLICATION

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II-Man BAE, Jae-Hoon KIM, Jae-Hyeong LEE  
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U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
<i>HMK</i>		5,812,491	9-22-1998	Shinozaki et al.		
		5,887,162	3-23-1999	Williams et al.		
		5,896,551	4-20-1999	Williams et al.		
		5,905,909	5-18-1999	Williams et al.		
		6,084,803	7-04-2000	Sredanovic et al.		
		6,178,501	1-23-2001	Ingalls		
<i>HMK</i>		US2001/0000820 A1	5-3-2001	Keeth et al.		

FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>

OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
<i>HMK</i>		Micron Technology, Inc., "MT41LC256K32D4 Synchronous Graphics Ram," Preliminary Specification booklet, 45 pgs., 1997.

Examiner: *H. Ho*

Date Considered: *3/12/05*

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